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09/928,194

2811

August 11, 2001

Kurt E. PETERSEN et al.

Application Number

First Named Inventor

Filing Date

Group Art Unit

Examiner Name To Be Assigned (to be used for all correspondence after initial filing) Total Number Of Pages In This Submission Attorney Docket No. 356952000304 **ENCLOSURES** (check all that apply) Assignment Papers After Allowance Communication to Fee Transmittal Form (for an Application) Group Appeal Communication to Board of Fee Attached Drawing(s) Appeals and Interferences Supplemental Preliminary Appeal Communication to Group Licensing-related Papers Amendment (Appeal Notice, Brief, Reply Brief) Petition **Proprietary Information** After Final Petition to Convert to a Affidavits/declarations Status Letter **Provisional Application** Power of Attorney, Revocation Other Enclosure(s) (please tdentify **Extension of Time Request** Change of Correspondence Address below): Terminal Disclaimer **Express Abandonment Request** Request for Refund Information Disclosure Statement CD, Number of CD(s) Certified Copy of Priority Document(s) Remarks Response to Missing Parts/ Incomplete Application Response to Missing Parts under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY OR AGENT Morrison & Foerster, LLP Firm Stephen C. Durant Individual Name Signature Date January 9, 2002

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Tina Hall

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Kurt E. PETERSEN et al.

Serial No.: 09

09/928,194

Filing Date:

August 11, 2001

For:

SINGLE CRYSTAL SILICON SENSOR WITH HIGH ASPECT RATIO AND CURVILINEAR STRUCTURES AND

ASSOCIATED METHOD

Examiner: To Be Assigned

Group Art Unit:

2811

SUPPLEMENTAL PRELIMINARY AMENDMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Prior to examination on the merits, Applicant respectfully requests entry of this Preliminary Amendment for the above-identified application.

IN THE CLAIMS;

Please cancel claims 1-54.

Please add the following new claims 55-77.

SUBCIOT

55. (New) A semiconductor micromechanical device comprising:

a first single crystal silicon wafer layer;